AMENDMENT AND RESPONSE UNDER 37 C.F.R § 1.111
Serial Number:10/578,659
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Title: DIGITAL SIGNAL PROCESSING METHODS AND APPARATUS

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Dkt: 1365.105US1

IN THE SPECIFICATION

On page 3, line 20, please amend the specification as follows:

Preferably the data in the data array represents a physical entity; it may be provided on a data carrier such as ~~an optical or electrical signal carrier, or on~~ any type of volatile or non-volatile storage medium. Some examples of this data are given below (the list is non-exhaustive):

On page 13, line 5, please amend the specification as follows:

Processor control code to implement the above described methods may be provided on a data carrier such as a disk, CD or DVD-ROM, <u>or</u> programmed memory such as read-only memory (Firmware) ~~or on a data carrier such as an optical or electrical signal carrier~~. The code may comprise code in a conventional programming language such as code for implementation by a digital signal processor (DSP) or code written in C, or microcode, or code for setting up or controlling an ASIC or FPGA, or code for a hardware description language such as Verilog (Trade Mark), VHDL or SystemC. As the skilled person will appreciate such code (and/or associated data) may be distributed between a plurality of coupled components in communication with one another.

On page 14 at line 20, please insert the following new paragraph:

Figure 12 shows a flowchart 1200 including: 1202 Input Target Matrix Data (X); 1204 Select a row of Matrix F and a column of Matrix G; 1206 Determine a target contribution R of the selected row and column to the Target Matrix; 1208 Determine, subject to a non-negativity constraint, updated values for the selected row and column from R; and 1210 All rows and columns of F and G updated?.